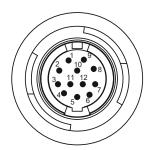


O:-L1	£	-1:-	C+	keaita

Pin	Belegung	
1	T1	
2	T1 invers	
3	T2	
4	T2 invers	
5	RI	
6	RI invers	
7	S1	
8	S2	
9	US invers	
10	Test	
11	0V	
12	V+	
Gehäuse	Schirm	



View on connecting side

Pin	Belegung
1	T1
2	T1 inverse
3	T2
4	T2 inverse
5	RI
6	RI inverse
7	S1
8	S2
9	US inverse
10	test
11	0V
12	V+
housing	shield

connection layout TTL Ei Pin Layout Change No: C115685-05

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